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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit 2183
Examiner David J. Huisman

In Re: Enric Musoll et al.
Case: P3810
Serial No.: 09/595,776
Filed: June 16, 2000
Subject: Methods and Apparatus for Improving Fetching and
Dispatch of Instructions in Multithreaded Processors

To: The Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir;

Response B

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Technology Center 2100

In the claims:

All of the claims standing for examination are presented below. Claims 1, 6, and 11 are herein amended in the present response.

31
1. (Currently amended) In a ~~multi-streaming~~ processor having multiple hardware streams supporting multiple data threads, and a data cache, a system for fetching instructions from individual ones of the multiple streams to a pipeline, comprising:

a fetch algorithm for selecting from which stream to fetch instructions; and

a hit/miss predictor for forecasting whether instructions will hit or miss the data cache;

wherein the prediction by the hit/miss predictor is used by the fetch algorithm in determining from which stream to fetch.